

**GEETHANJALI COLLEGE OF ENGINEERING AND TECHNOLOGY, CHEERYAL, KEESARA, MEDCHAL
(AUTONOMOUS)**

Department of Electronics and Communication Engineering.

Minutes of the Board of Studies (BoS)

Venue: Board Room

Time: 11AM -2PM

Date: August 11, 2018

Members Present

JNTUH Nominee:	Dr. L.Pratap Reddy, Professor, ECE, JNTUH, Hyderabad	<i>[Signature]</i>
Subject Expert1 (from Academia):	Dr.C.B.Rama Rao, Professor, ECE, NIT Warangal	<i>[Signature]</i>
Subject Expert2 (from Academia):	Dr. Anil Kumar Vuppala IIIT, Hyderabad	<i>[Signature]</i>
Subject Expert3 (from Industry) :	Sri C.Ramesh Reddy, Scientist "F", RCI, DRDO, Hyderabad	<i>[Signature]</i>
Chairman, College Academic Council:	Dr S.Udaya Kumar, Principal, GCET	<i>[Signature]</i>
Chairman, ECE Dept BOS:	Prof. B. Hari Kumar, HOD, ECE, GCET	<i>[Signature]</i>
GCET Alumni Representative,:	Mr.RVNR Suneel Krishna	<i>[Signature]</i>

Internal Members of the ECE Dept. GCET:

- | | | | |
|------------------------------------|--------------------|---|--------------------|
| 1. Dr.R.S.Raju, Professor | <i>[Signature]</i> | 6. Mr. OVPR Siva Kumar, Professor | <i>[Signature]</i> |
| 2. Mr.K.Somasekhara Rao, Professor | <i>[Signature]</i> | 7. Dr.V.Satya Srinivas, Associate Professor | <i>[Signature]</i> |
| 3. Dr.C.V.Narasimhulu, Professor | <i>[Signature]</i> | 8. Mr.D.Venkata Rami Reddy, Associate Professor | <i>[Signature]</i> |
| 4. Dr.S.Suryanarayana, Professor | <i>[Signature]</i> | | |
| 5. Dr.P. Vijai Bhaskar, Professor | <i>[Signature]</i> | | |

Members Not Present: Dr. S. Ramana Murthy, Professor

Minutes:

Welcoming Members by the HoD ECE Dept.: Prof. B. Hari Kumar

The Chairman for the ECE Department BoS and the Head of the Department (HoD): Prof. Hari Kumar welcomed members to the BoS Meeting. The meeting started with the introduction of BoS members by HoD..

- 1. Discussion and seeking the Approval of proposed Program Structure and the detailed syllabi for II Year B.Tech (ECE) and service Courses under Autonomous status (vide AR18):**

Program Structure:

Members suggested the following changes in the structure:

1. It is suggested to combine Analog Communications and Digital Communications courses into a single course, to provide scope to offer "Operating Systems" as a core course.
2. It is suggested to offer VLSI Design as Professional Elective Course instead of Program Core Course and suggested to change the name of "VLSI Lab" as "Project Oriented Lab".
3. It is suggested to add one tutorial period to Digital Signal Processing and Computer Network Courses.
4. It is suggested to include one internship program with two credits during summer vacation after Second Year Second Semester and evaluate in Third Year First Semester.

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5. Digital Design lab which was software lab earlier now converted into hardware lab consisting of the experiments pertaining to Digital Integrated Circuits.
6. Simulation lab consists of experiments related to Analog Communications and Probability Theory and Stochastic Processes and it is purely a software based lab.
7. Management Fundamentals course is introduced in second year second semester for nearly balancing the credits required under HSMC category as per AICTE model curriculum.
8. Analog Communications and Digital Communications courses are combined as a single course with name "Analog and Digital Communications" which is offered in second year first semester.
9. Operating Systems course is introduced in third year first semester and the syllabus is to be framed and approved by CSE department Board of Studies.
10. It is suggested to change the PE-I course name from "Information Theory and Coding" to "Principles of Information Theory".
11. It is suggested to include DBMS, RTOS and IoT in Professional Electives, if possible.
12. VLSI Lab is changed to Project Oriented Lab.

The meeting is concluded with gratitude towards the Board of Studies members and all are joined for lunch.

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